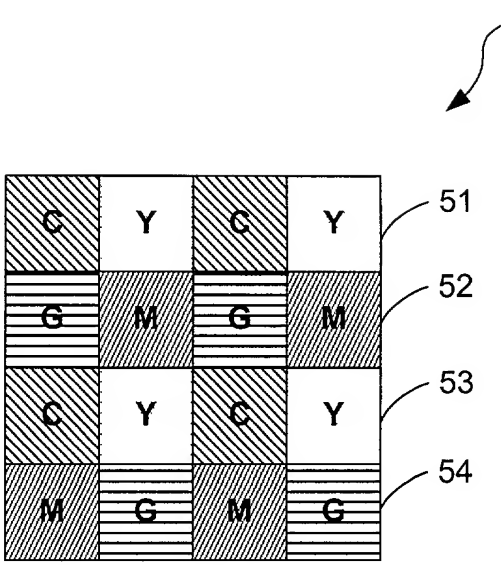
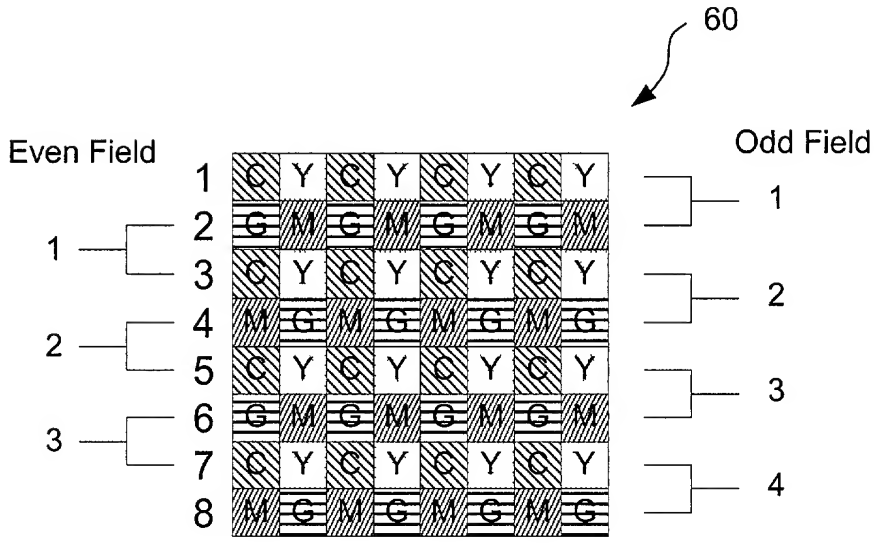



Figure 1

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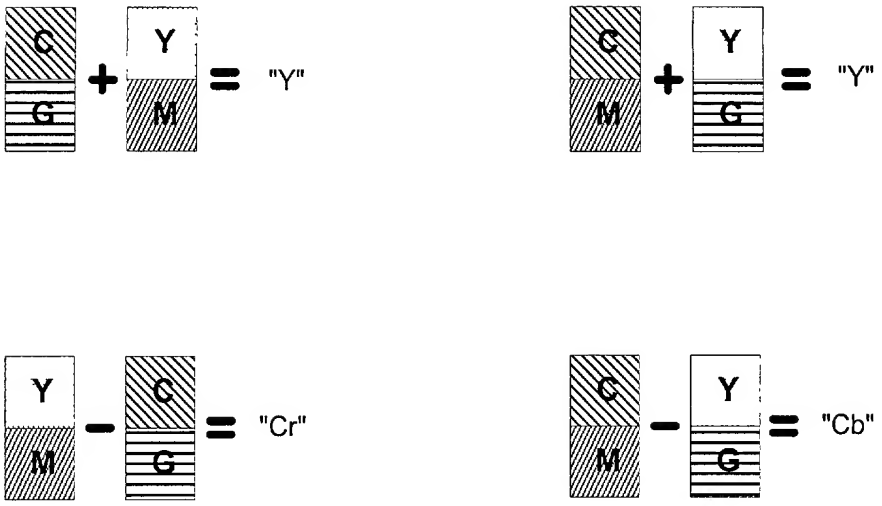


***Figure 3***

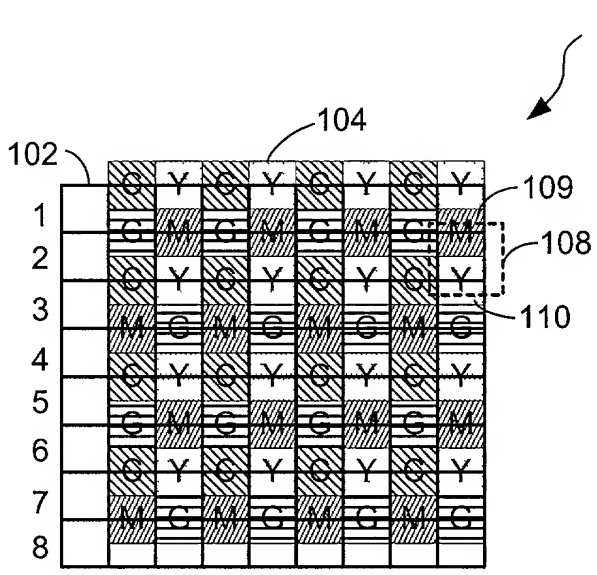


***Figure 4***

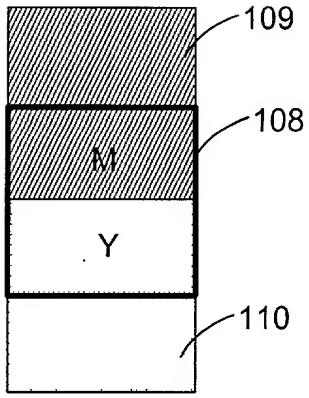


Figure 5

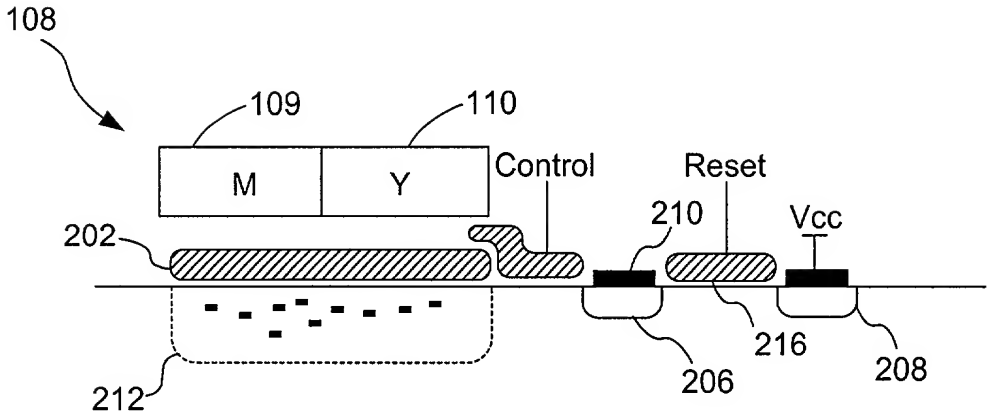


Figure 6

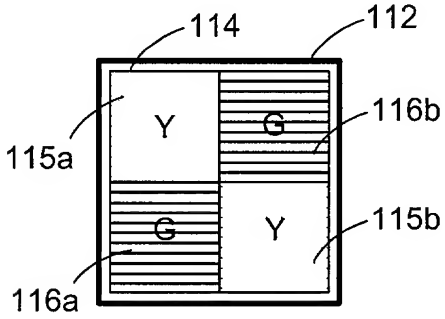


Figure 7

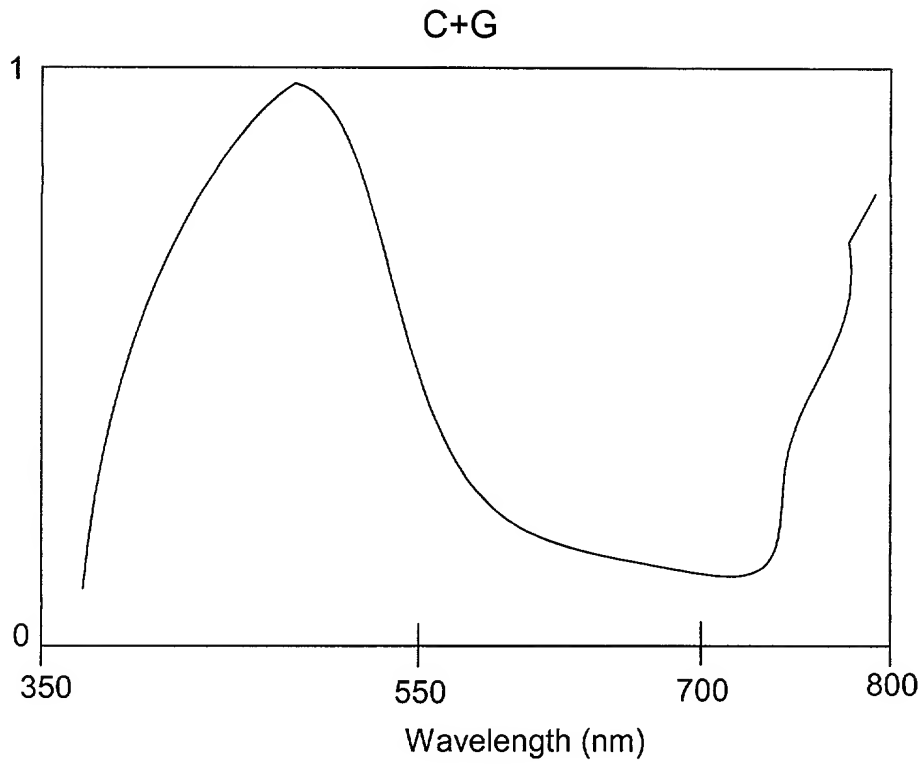


***Figure 8a***

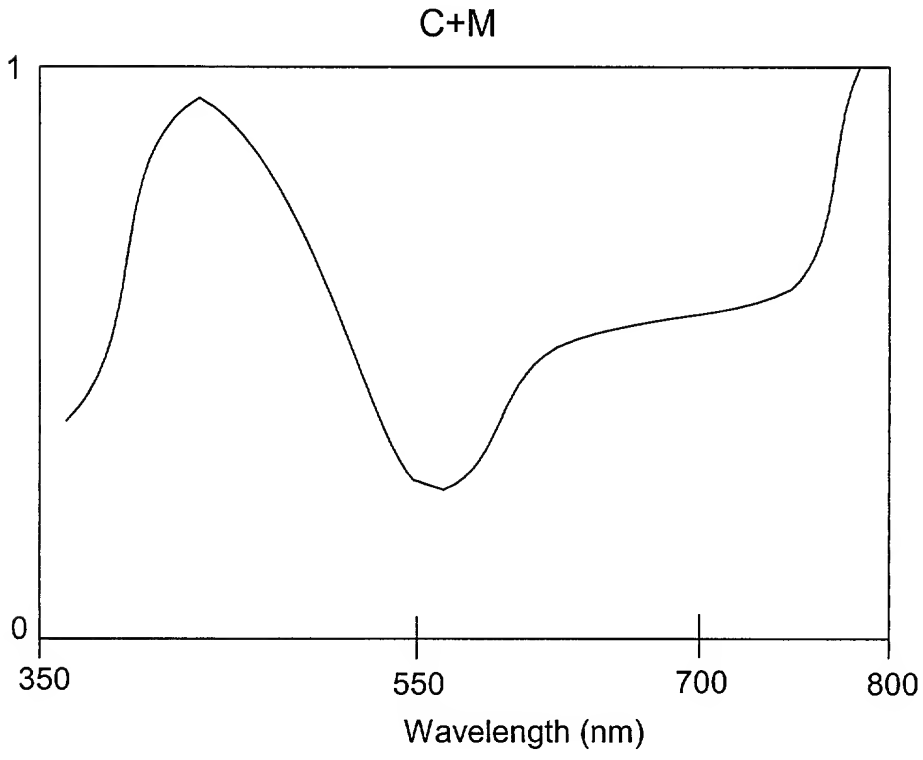


***Figure 8b***

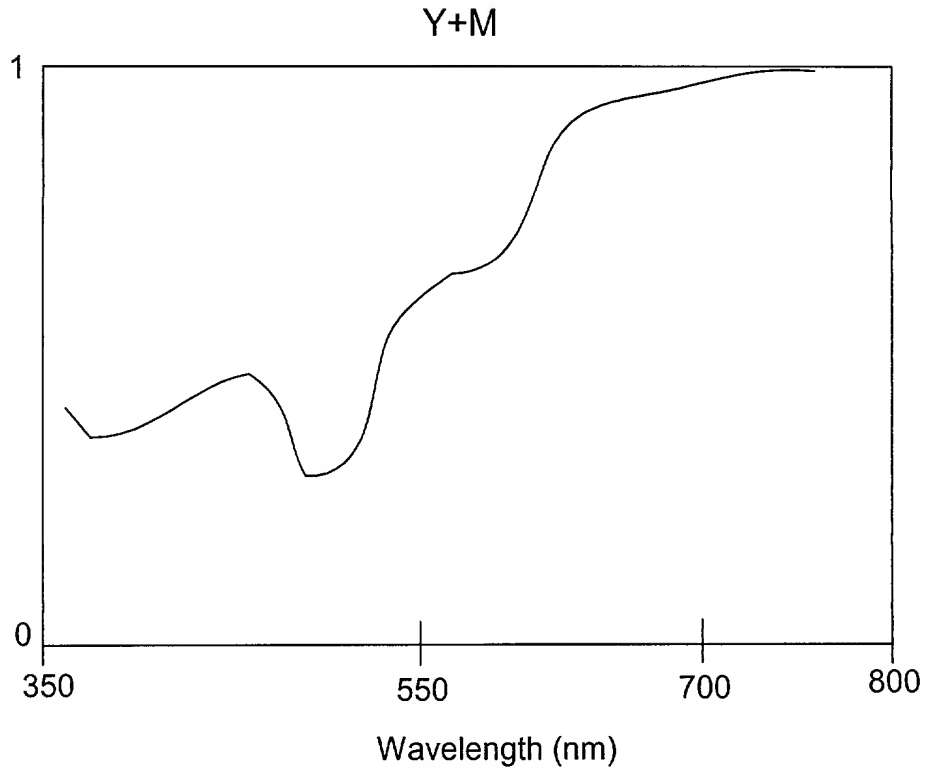


***Figure 8c***

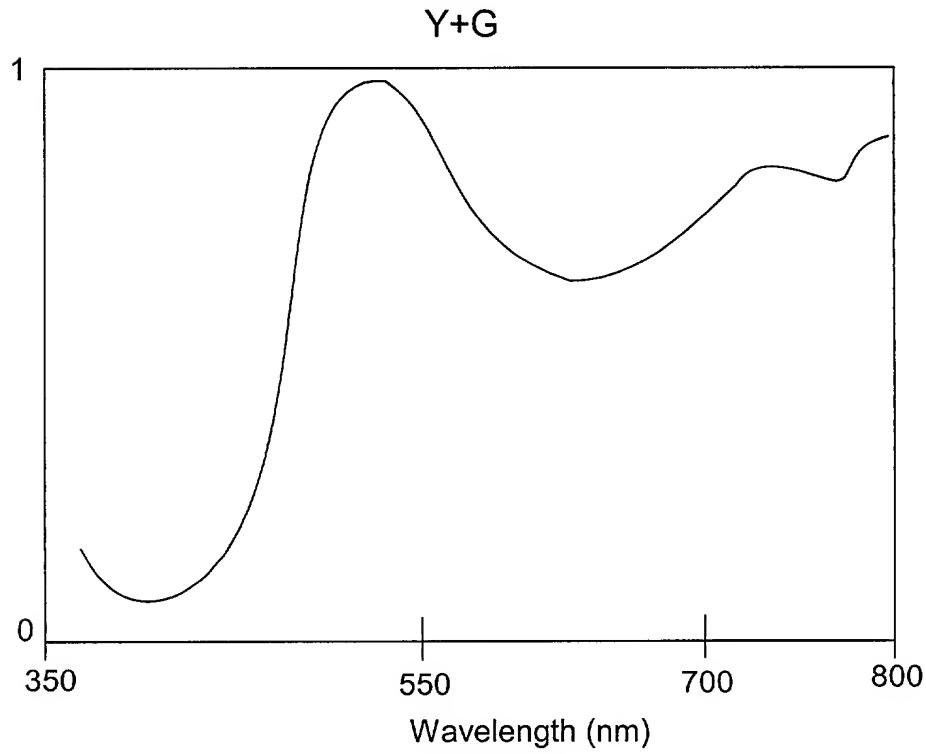


***Figure 8d***

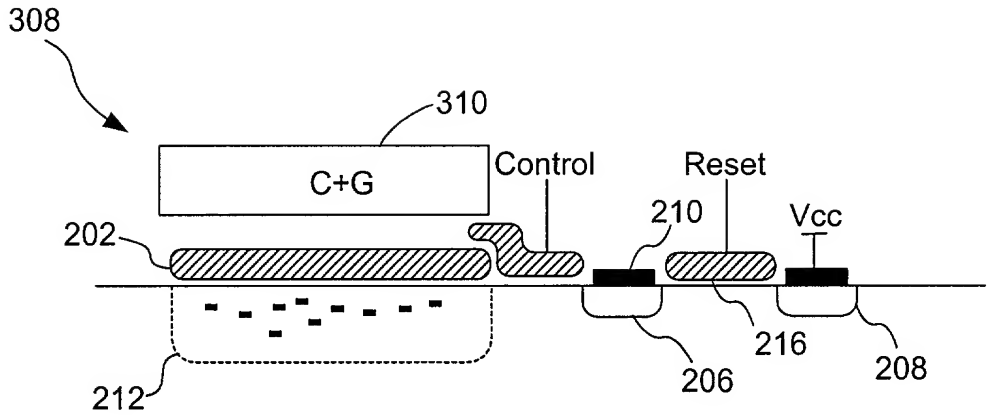


***Figure 9***

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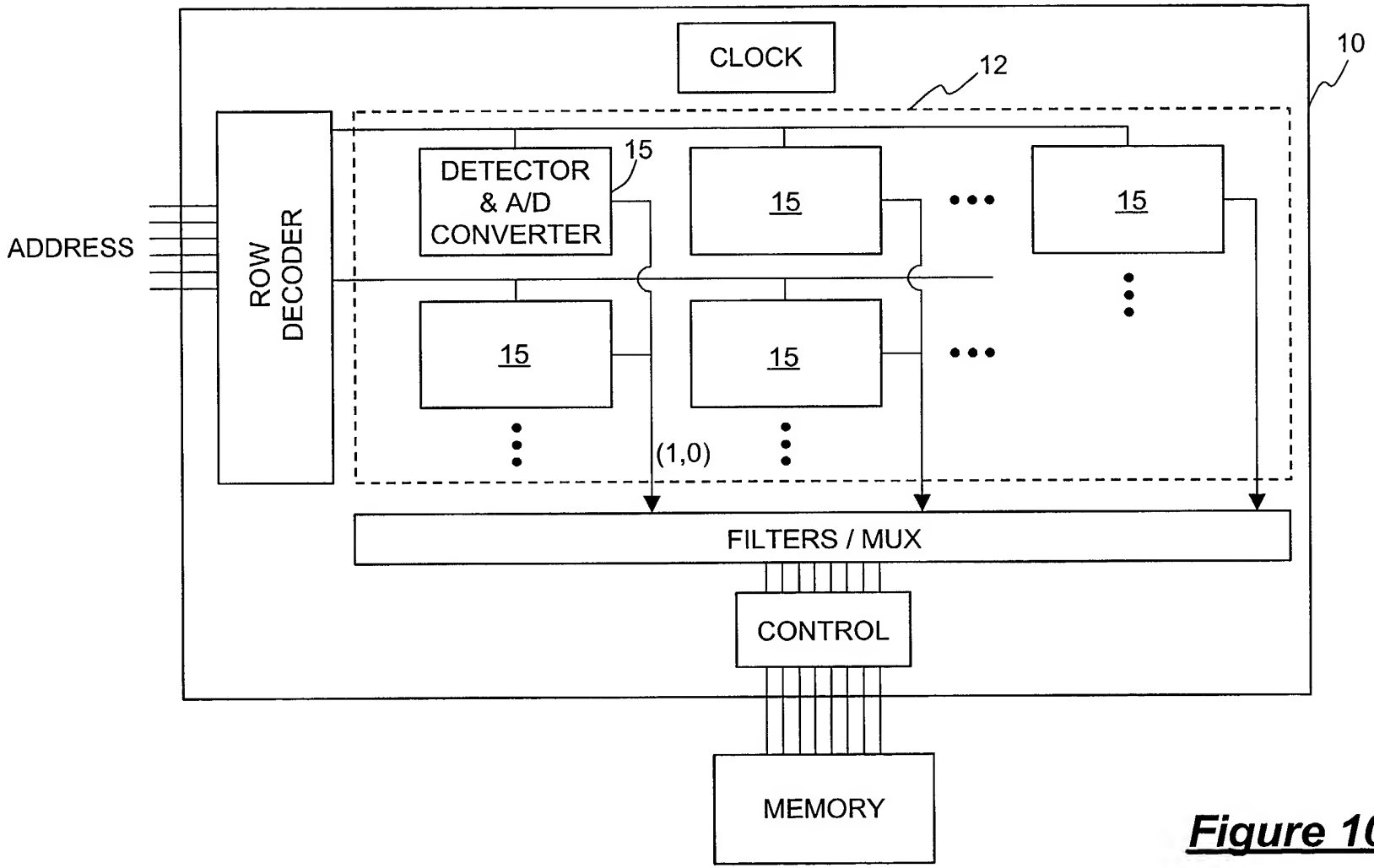
CLOCK
12
10
ADDRESS
ROW DECODER
DETECTOR & A/D CONVERTER
15
15
15
15
15
(1,0)
FILTERS / MUX
CONTROL
MEMORY

Figure 10